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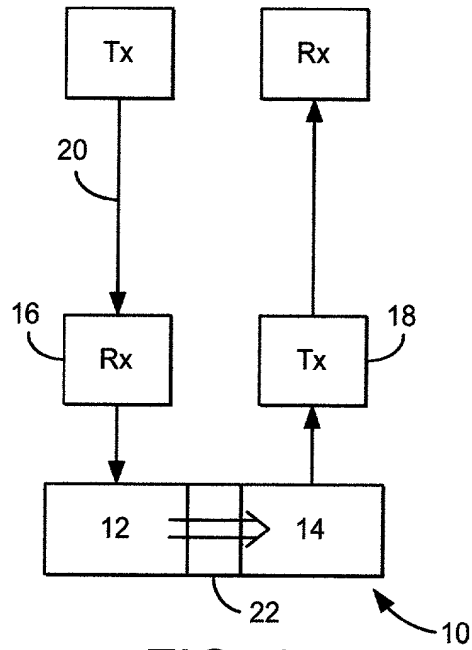


FIG. 1  
Prior Art

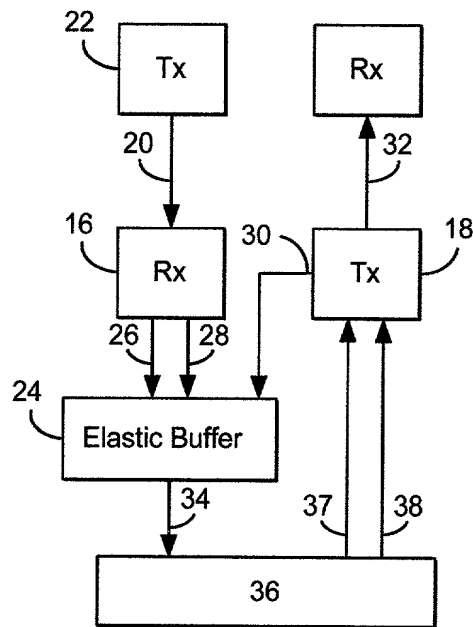


FIG. 2

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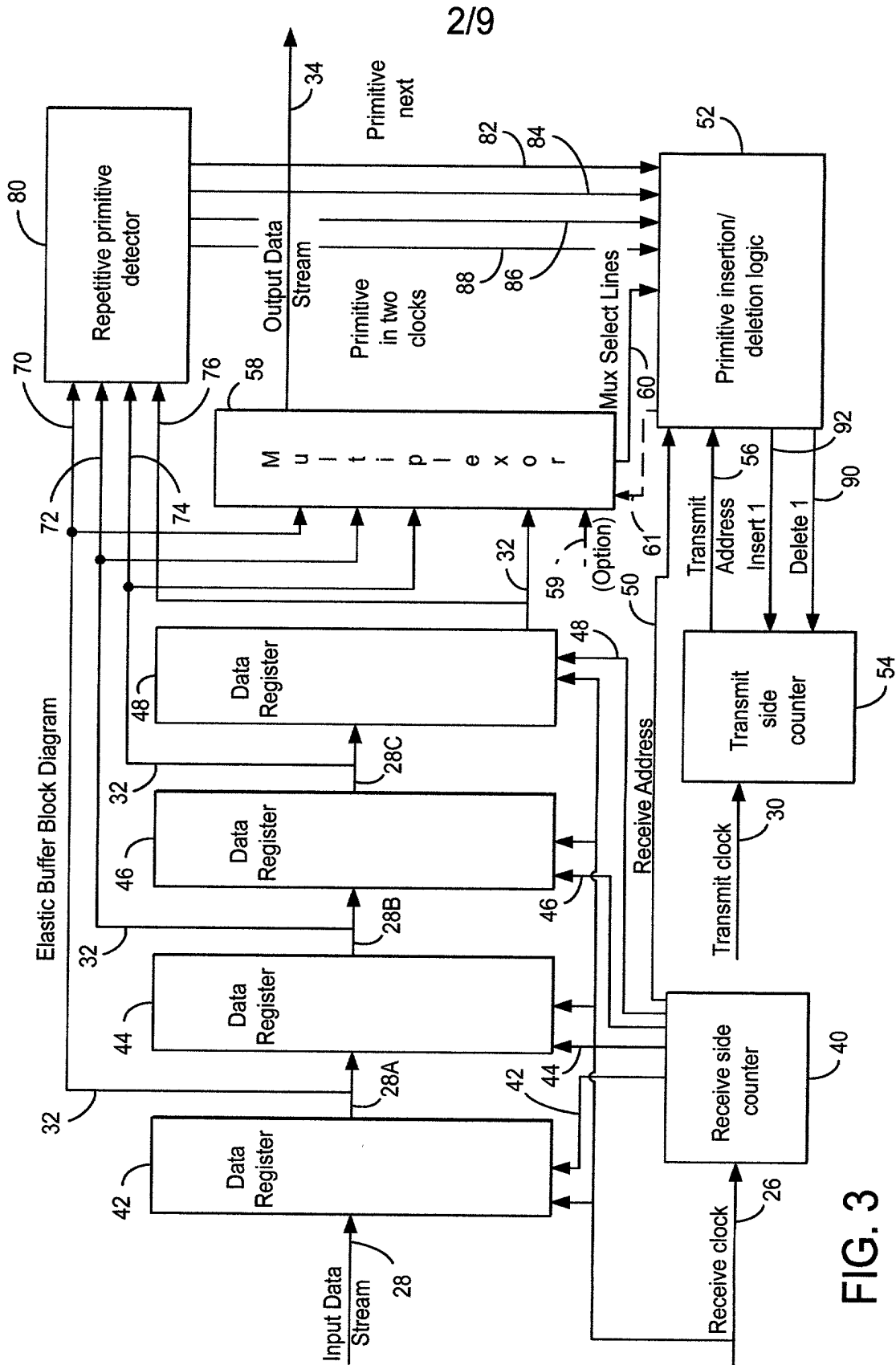
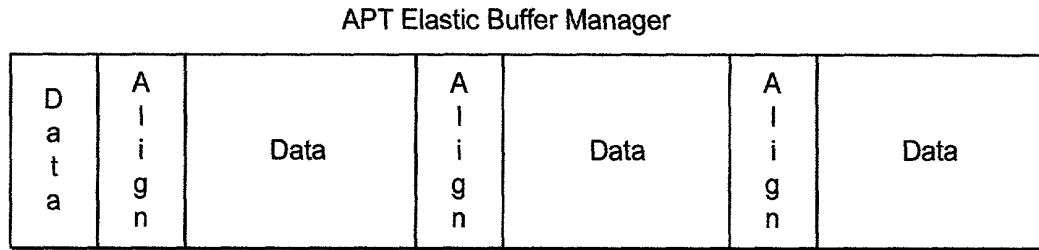
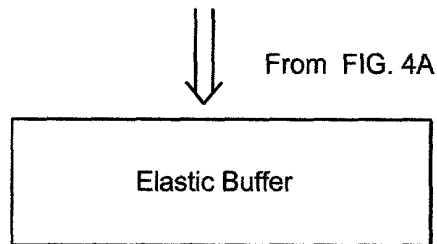
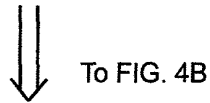


FIG. 3

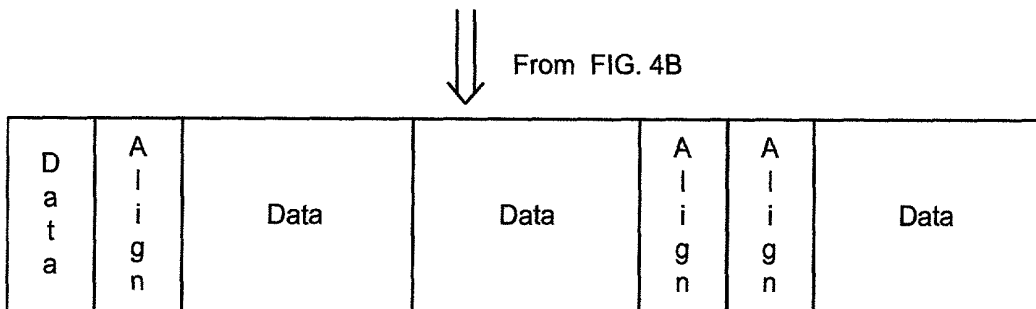
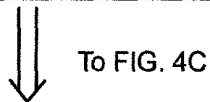
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**FIG. 4A**  
Input data stream



**FIG. 4B**



**FIG. 4C**  
Output data stream

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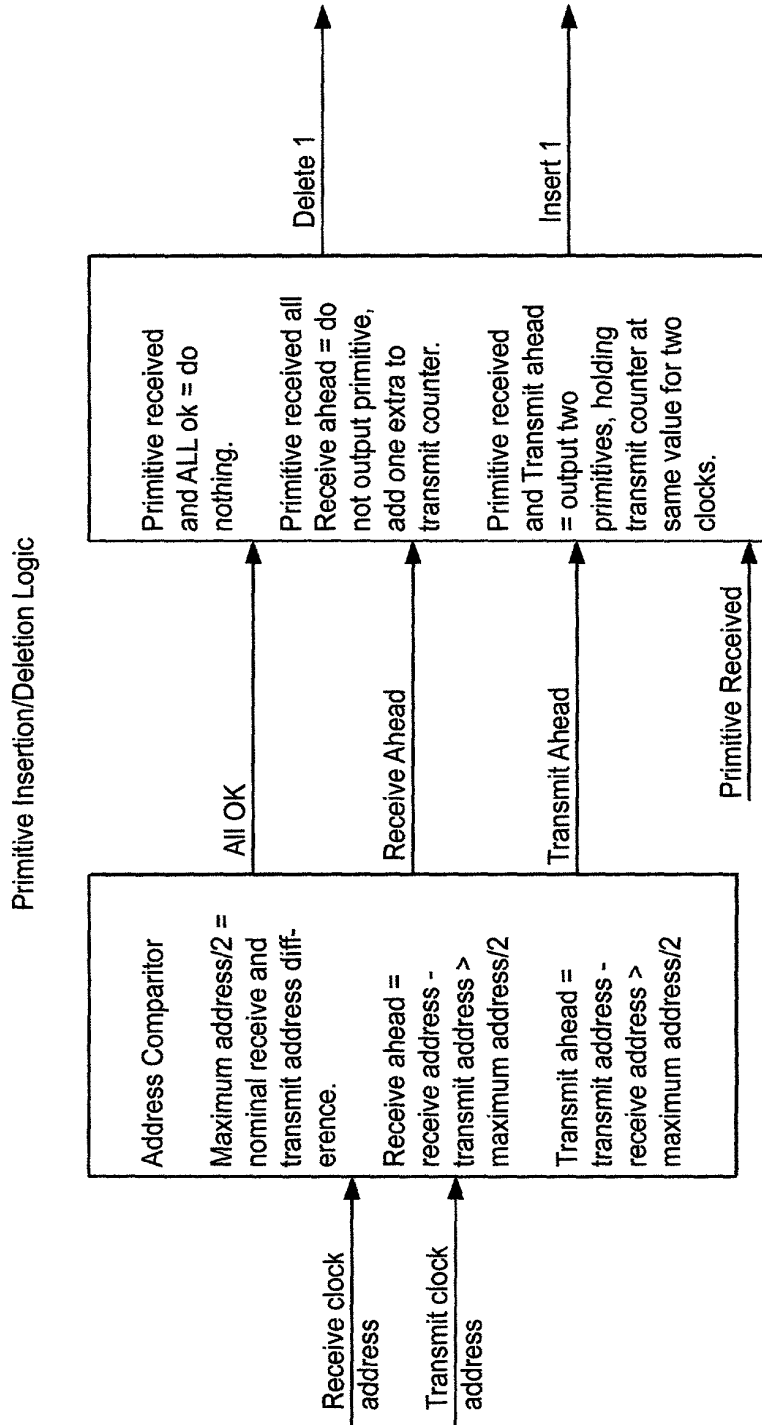
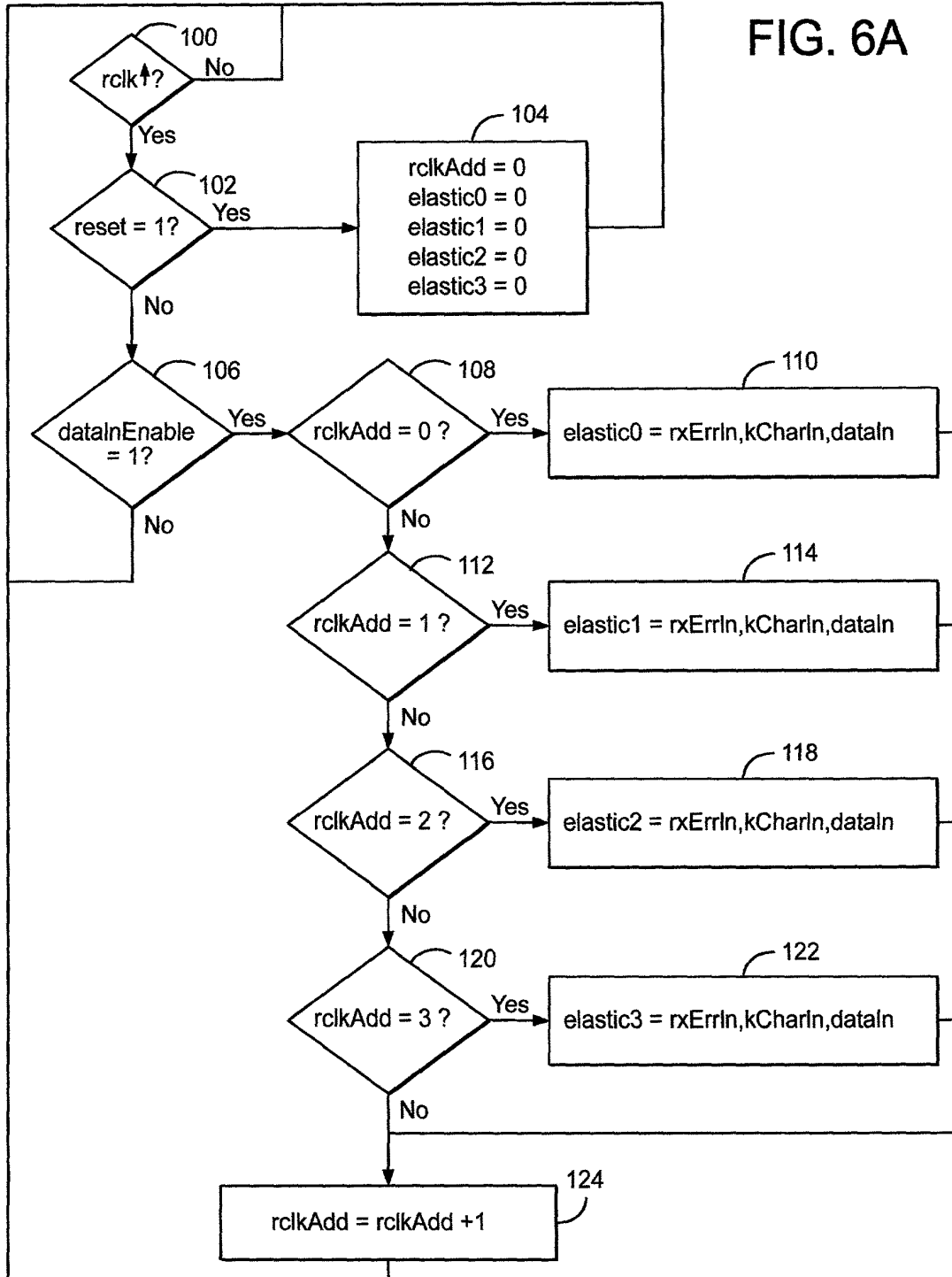


FIG. 5

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FIG. 6A



Title

the memories, 4 words, written by rclk, and  
 read by clk375, with an address difference of  
 two, or one at worst

FIG. 6A

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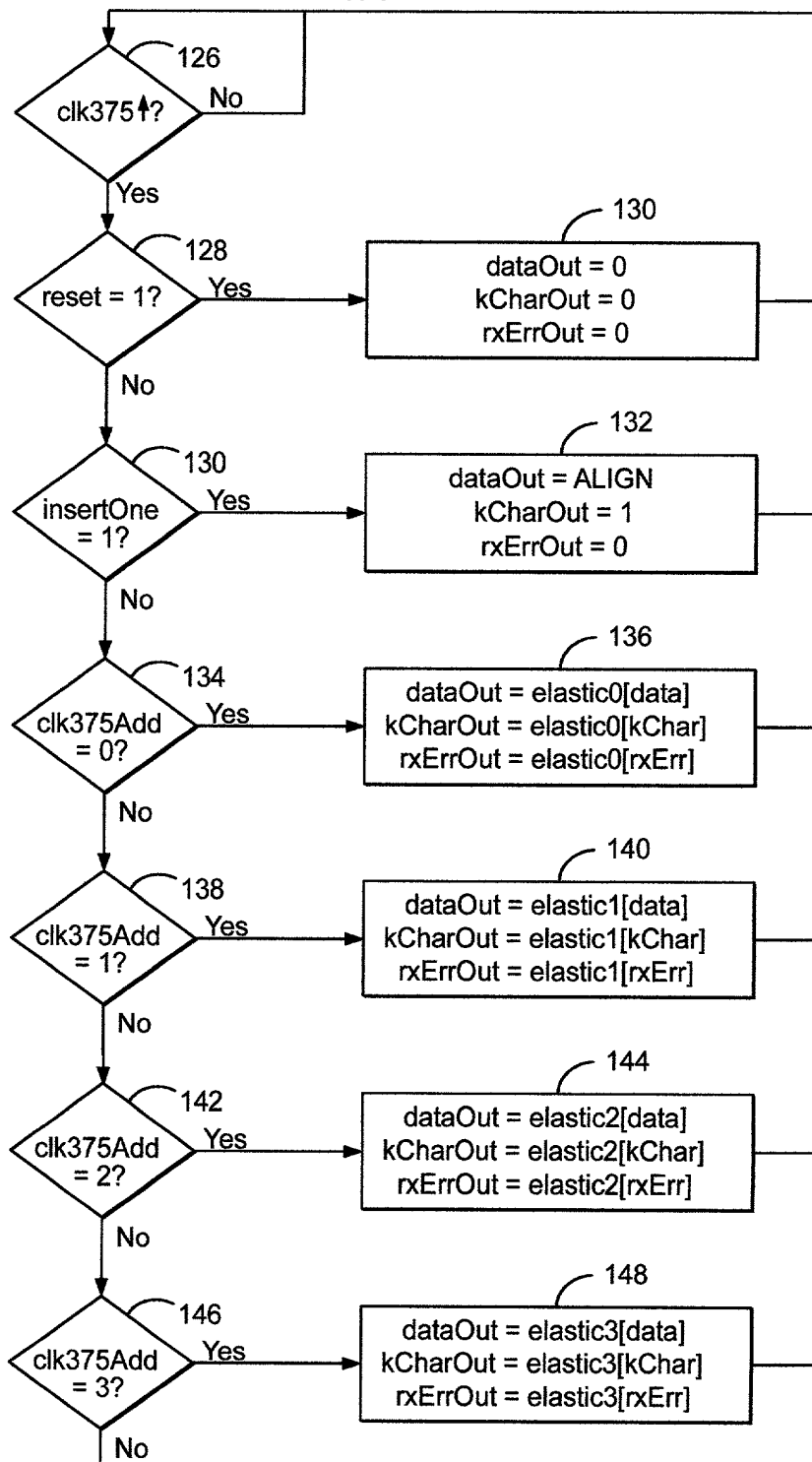
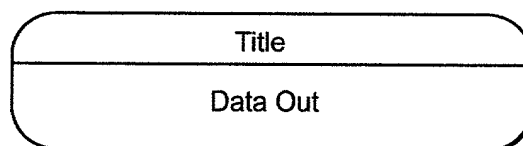


FIG. 6B



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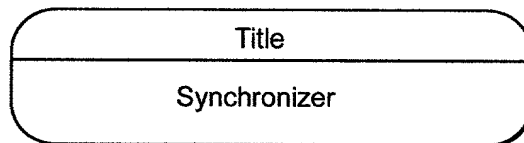
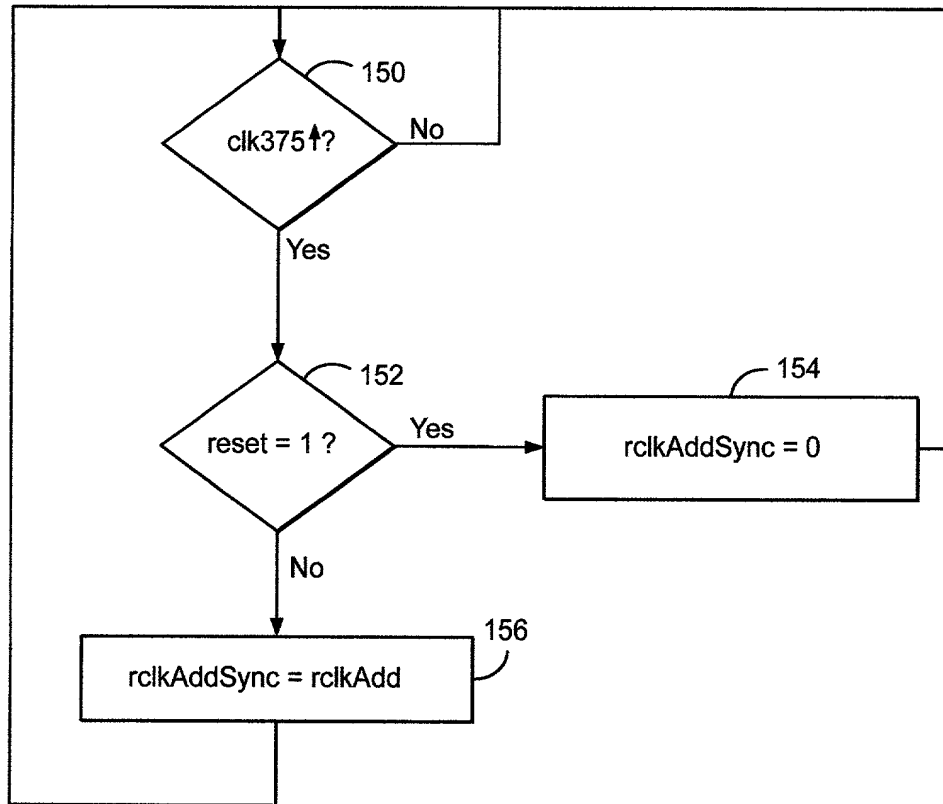


FIG. 6C

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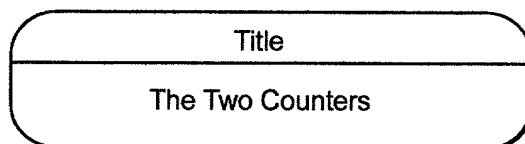
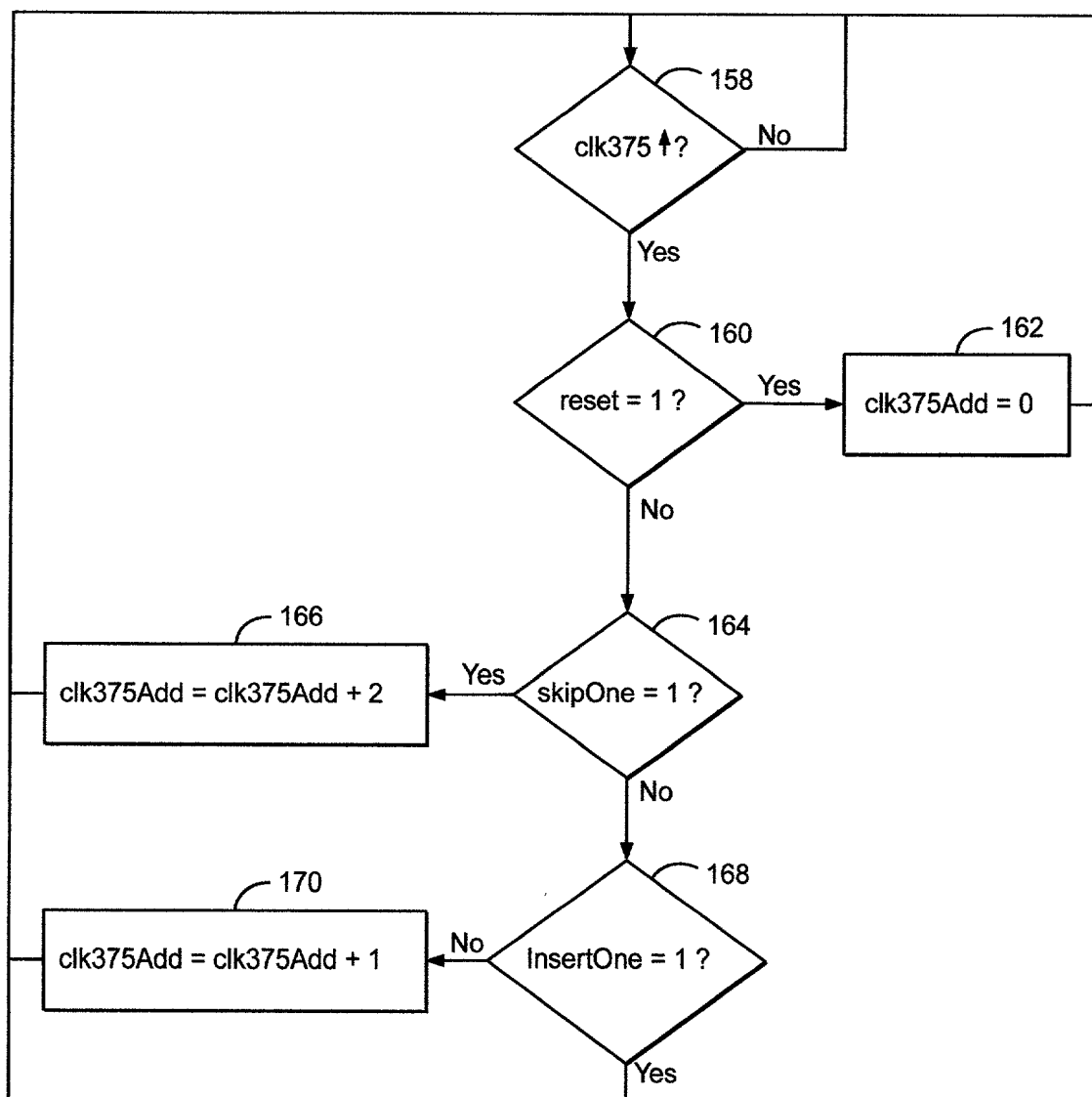
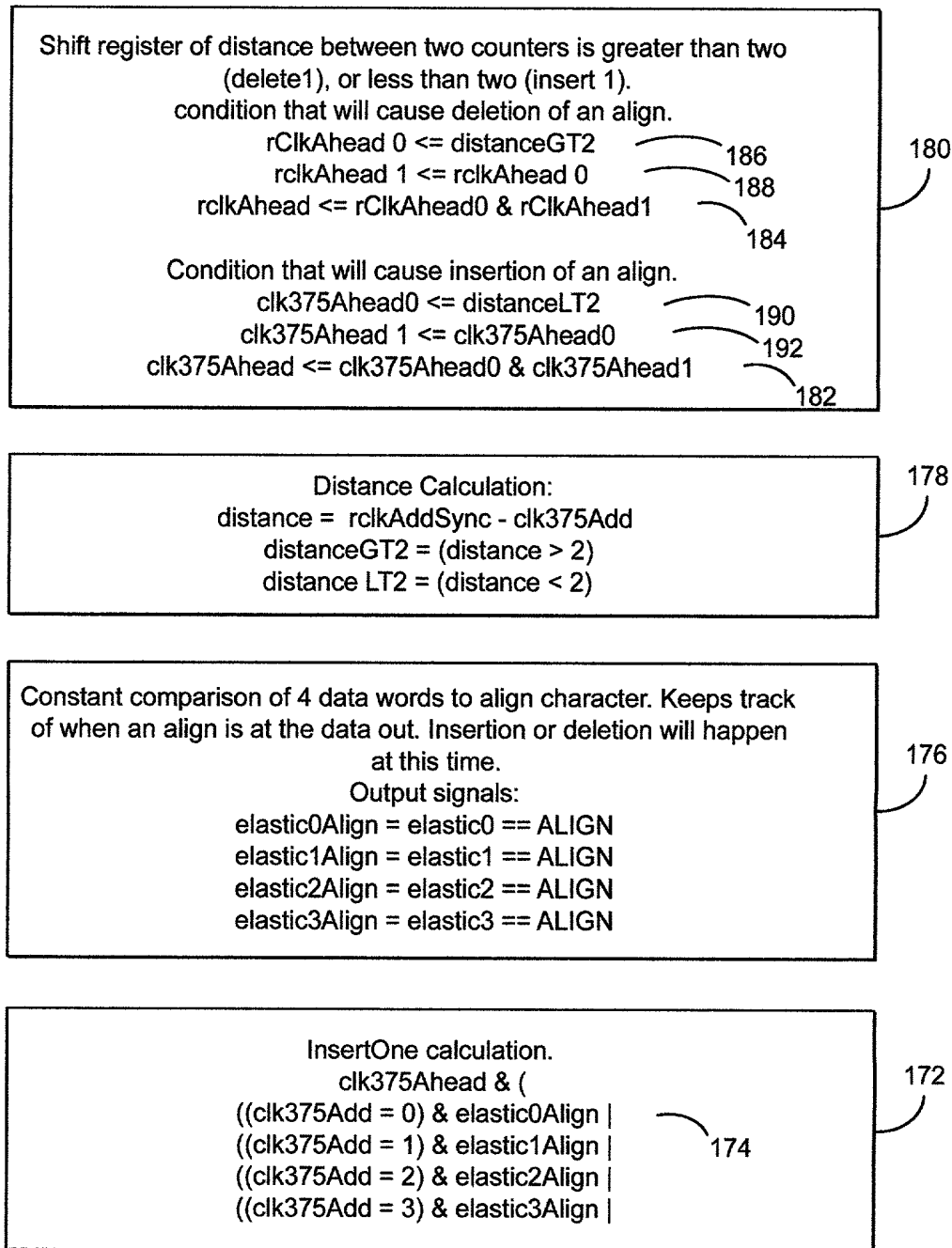


FIG. 6D



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Title
Insert/delete comparisons

FIG. 7